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***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

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In re application of: Banks et al.

Attorney Docket No.: CISC348/8110

Application No.: 10/723,684

Examiner: Chavis, John Q.

Filed: November 25, 2003

Group: 2193

Title: CONFIGURATION SYNCHRONIZATION  
FOR REDUNDANT PROCESSORS EXECUTING  
DIFFERENT VERSIONS OF SOFTWARE

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**SEPARATE LETTER TO EXAMINER  
REQUESTING DRAWING APPROVAL**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Due to informalities in the original drawings enclosed are substitute (formal) drawings for the above-identified patent application. If the Draftsman has any question concerning these drawings, he or she is respectfully requested to contact the undersigned.

Respectfully submitted,  
BEYER WEAVER LLP

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